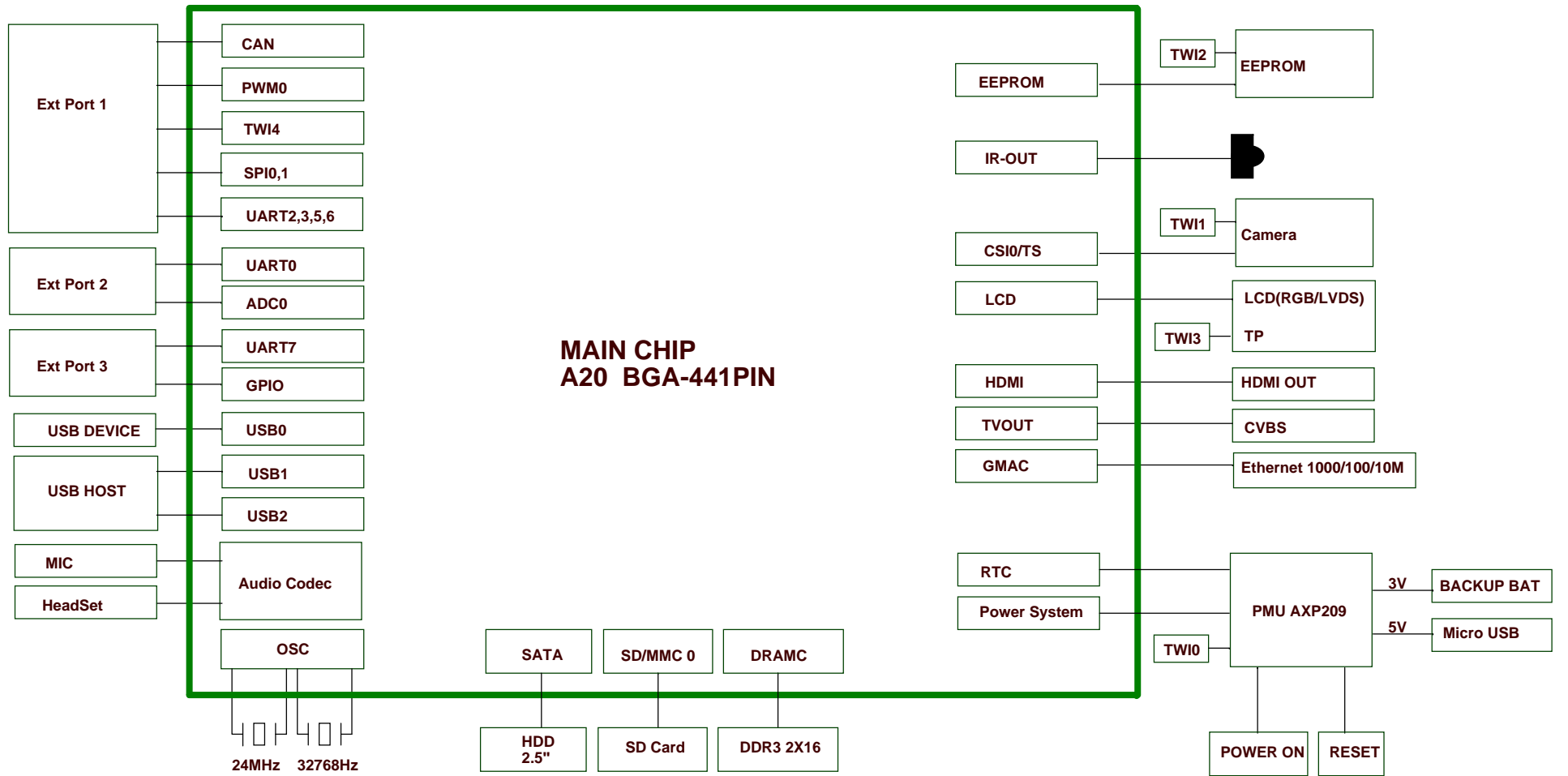


BLOCK



Differential pairs
Z0= 100ohm +/-5 ohm



Differential pairs
Z0= 90 ohm +/-5 ohm

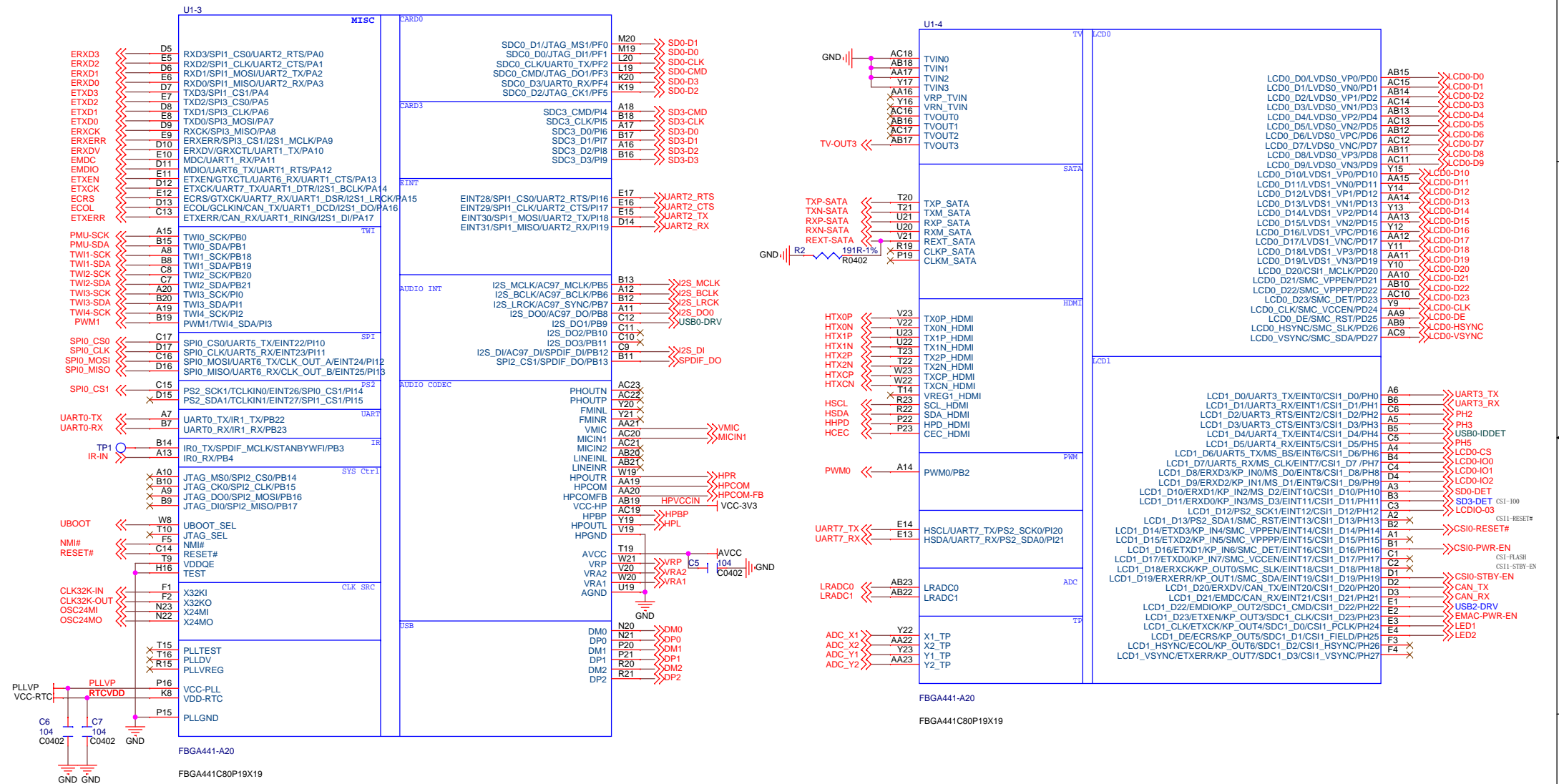


Z0= 50ohm +/-5 ohm

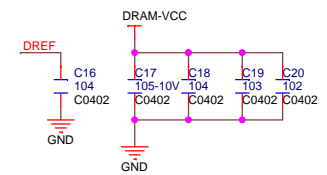
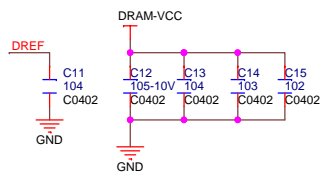
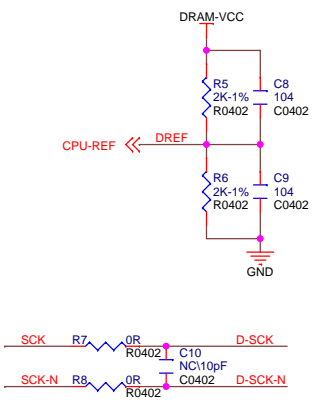
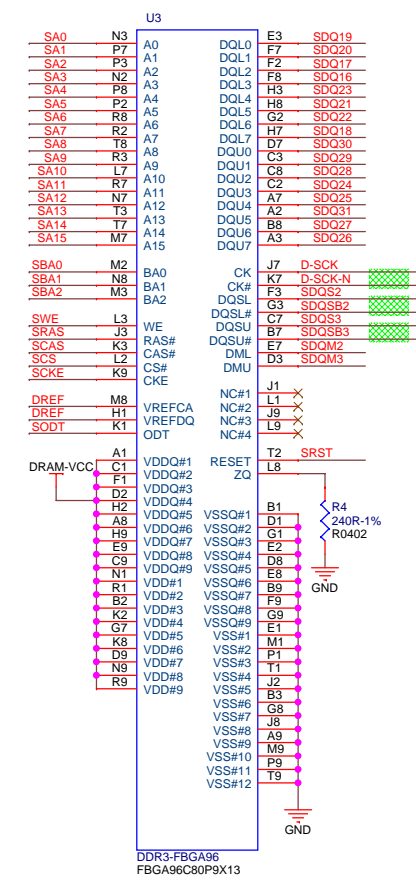
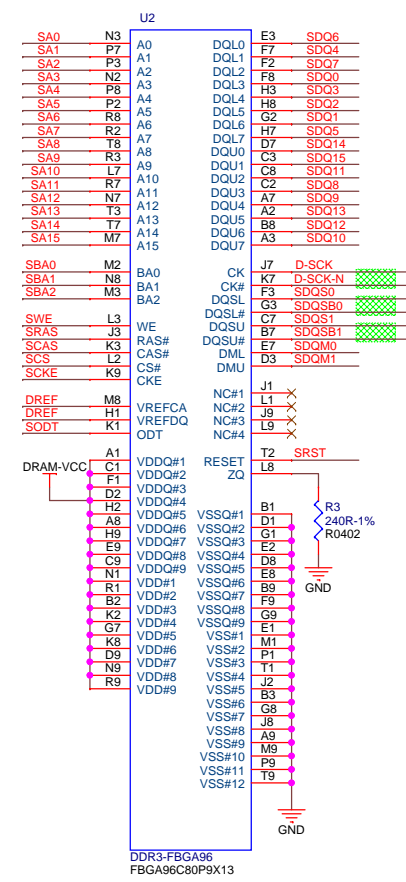
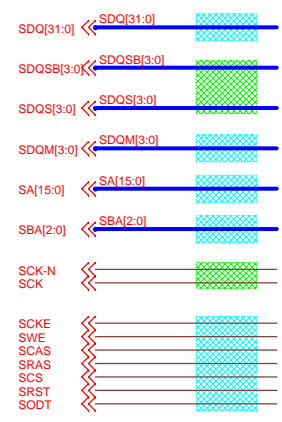


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CPU2

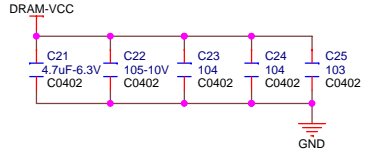


DDR3-16BITX2

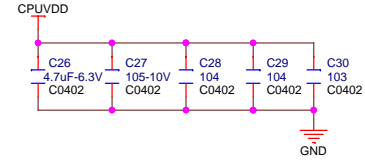


BESIDE CPU

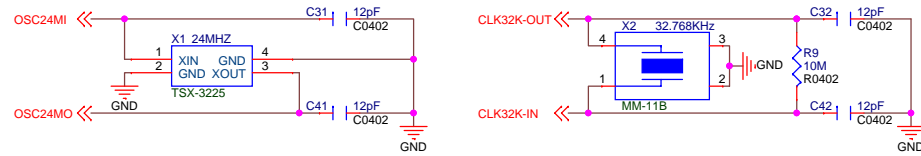
DRAM



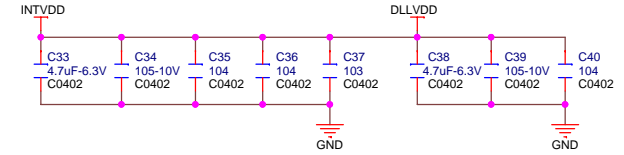
CPU



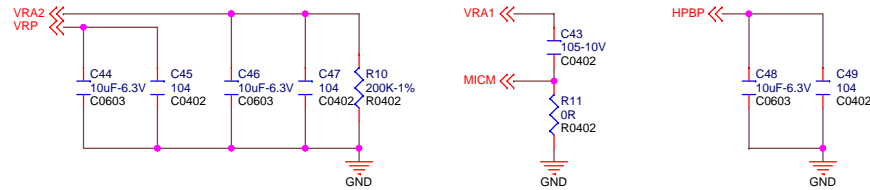
Crystal



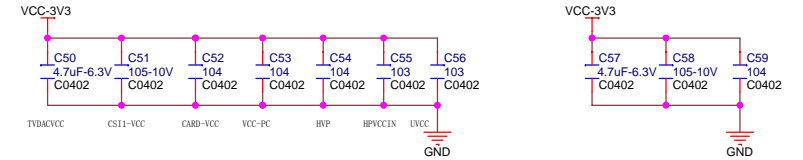
CORE



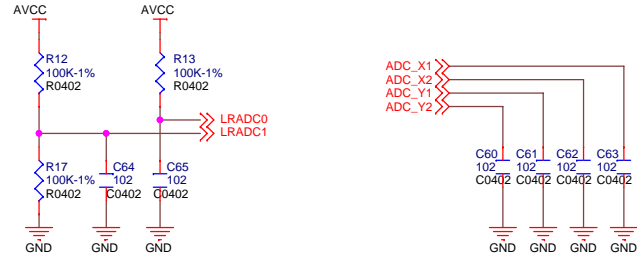
AUDIO



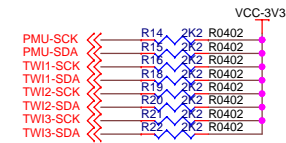
PIO-INTERFACE



ADC



TWI-PULLUP



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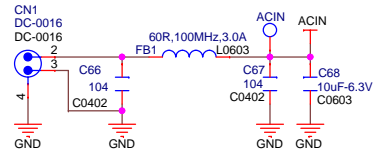
5

of

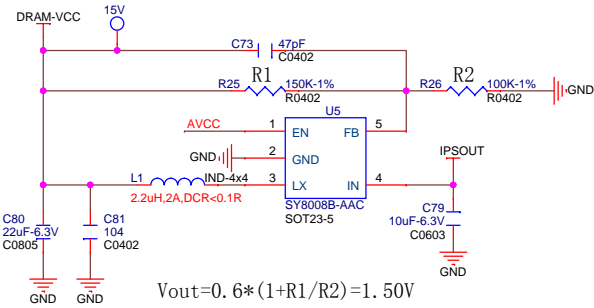
13

Power

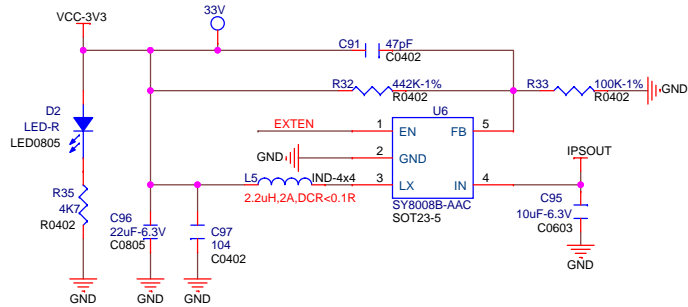
DC-IN



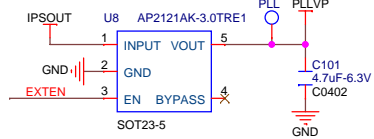
DRAM-VCC



VCC-3V3

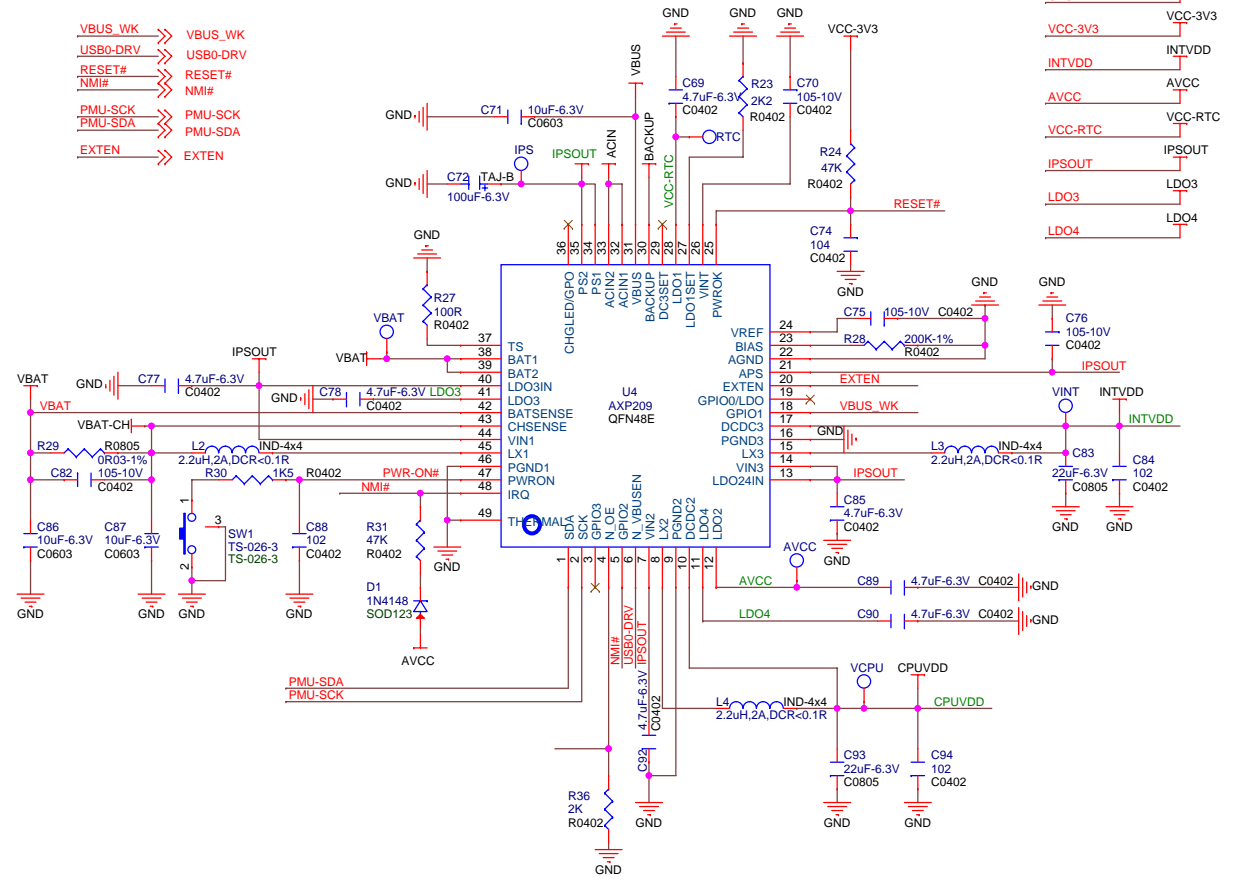


VCC-PLL

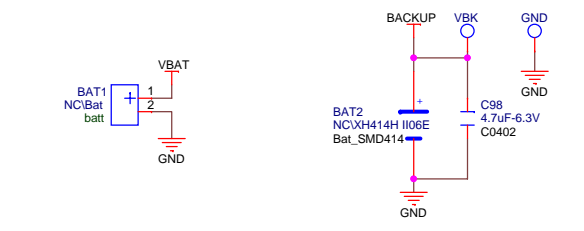
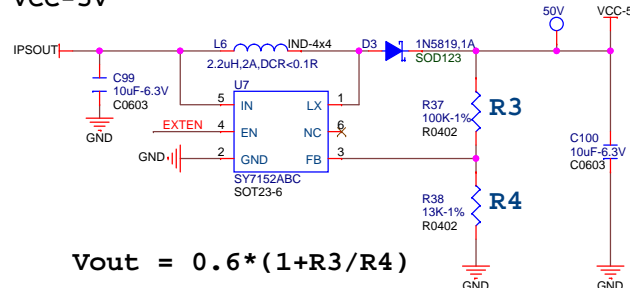


PMU-AXP209

- VBUS_WK >>> VBUS_WK
- USB0-DRV >>> USB0-DRV
- RESET# >>> RESET#
- NMI# >>> NMI#
- PMU-SCK >>> PMU-SCK
- PMU-SDA >>> PMU-SDA
- EXTEN >>> EXTEN



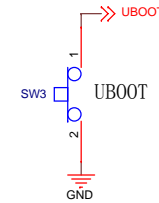
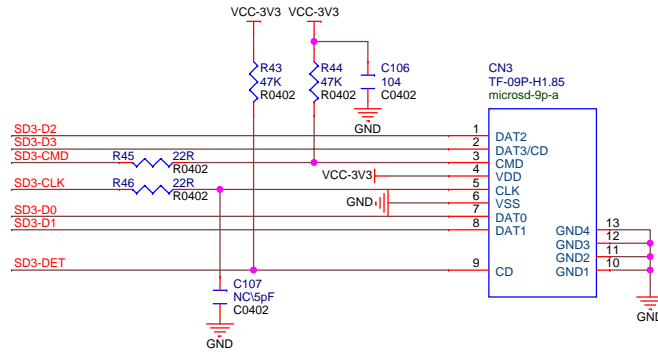
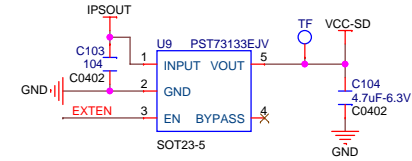
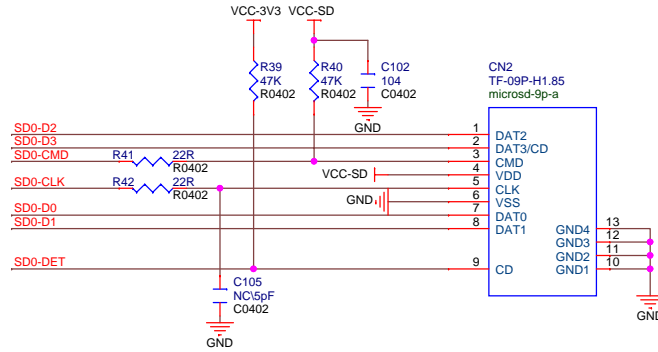
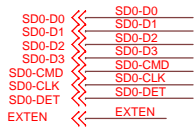
VCC-5V



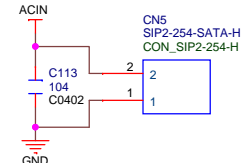
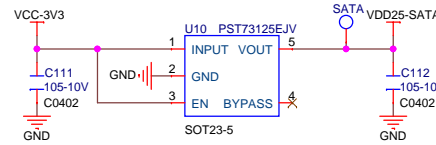
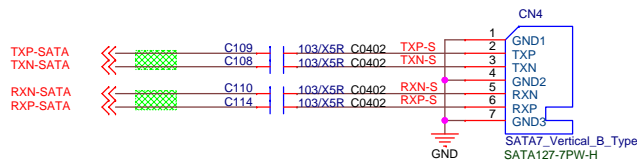
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TF-SATA

TF CARD



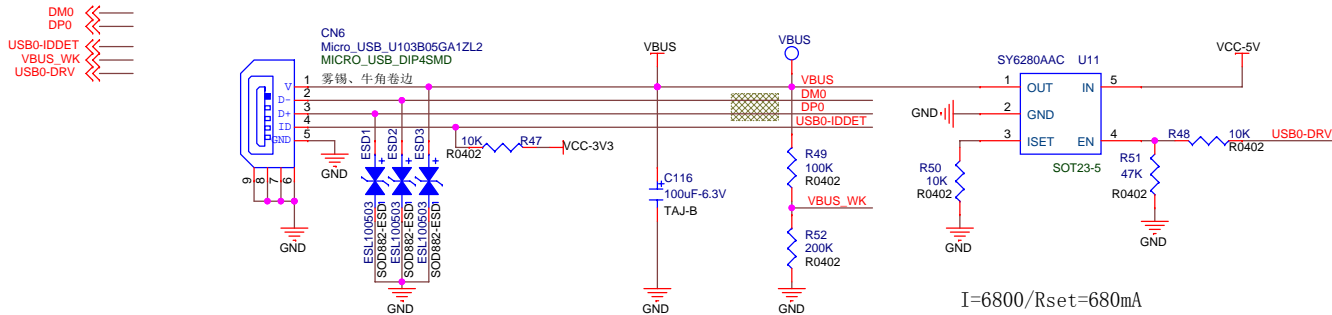
SATA



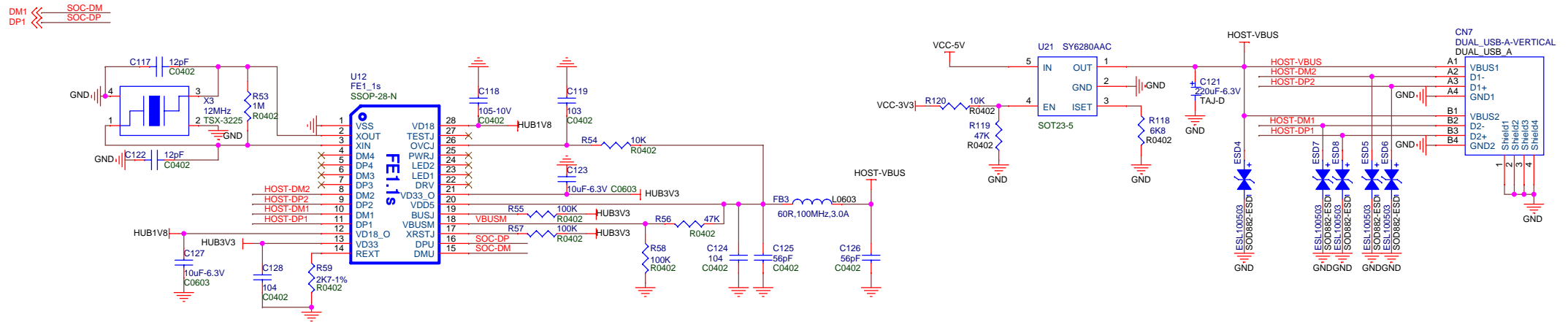
Xunlong Software		
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USB-WiFi

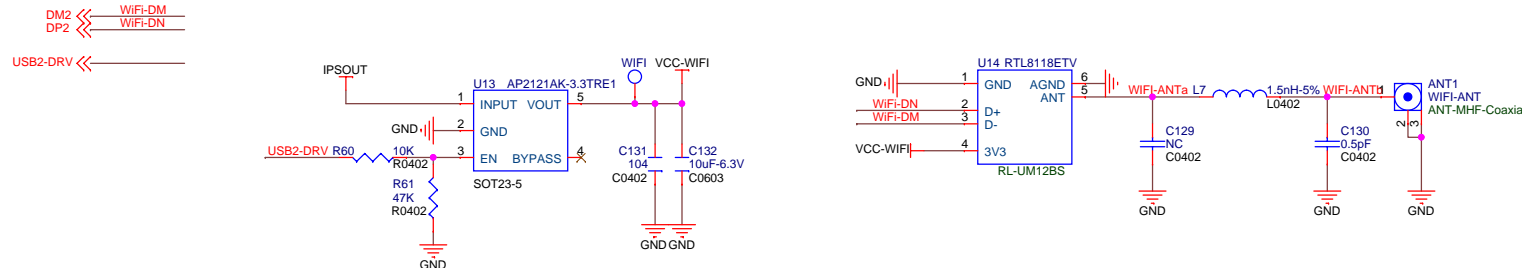
USB OTG



USB HOST



USB WIFI

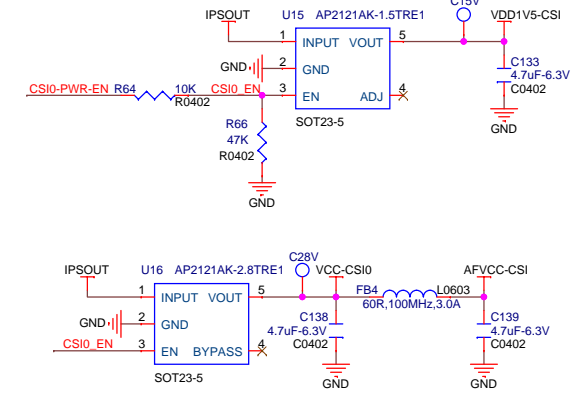
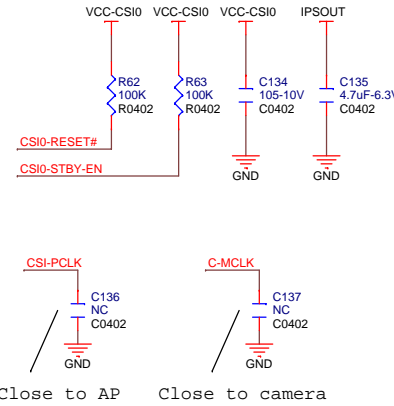
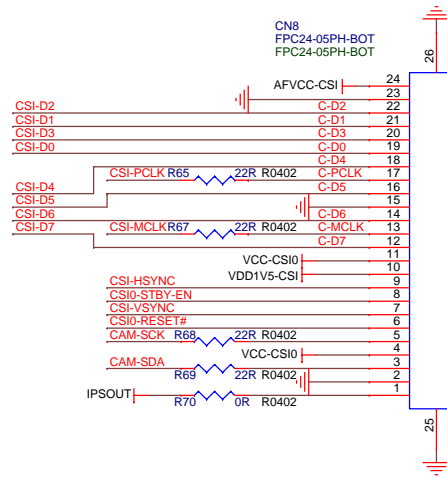
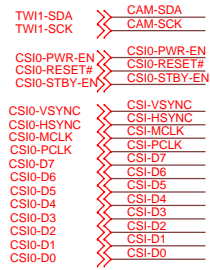


Xunlong Software

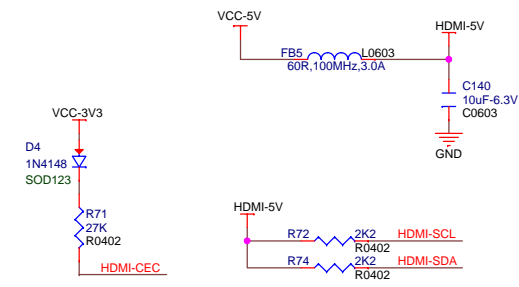
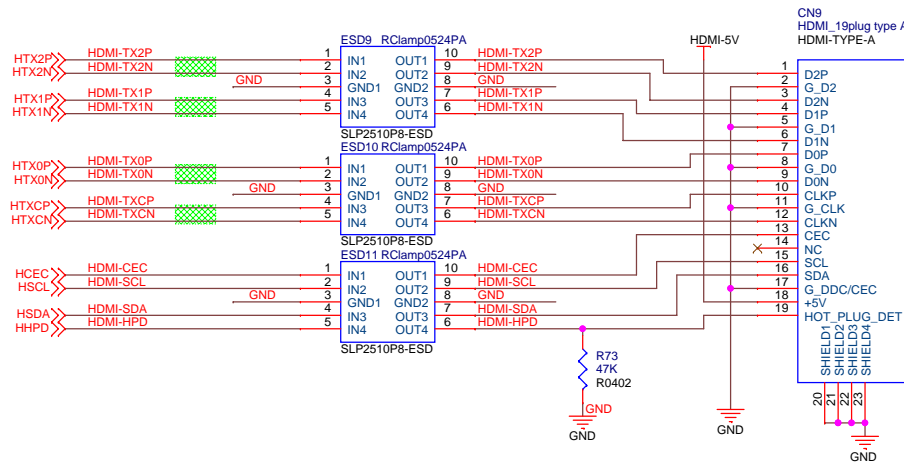
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CSI-HDMI

CSI

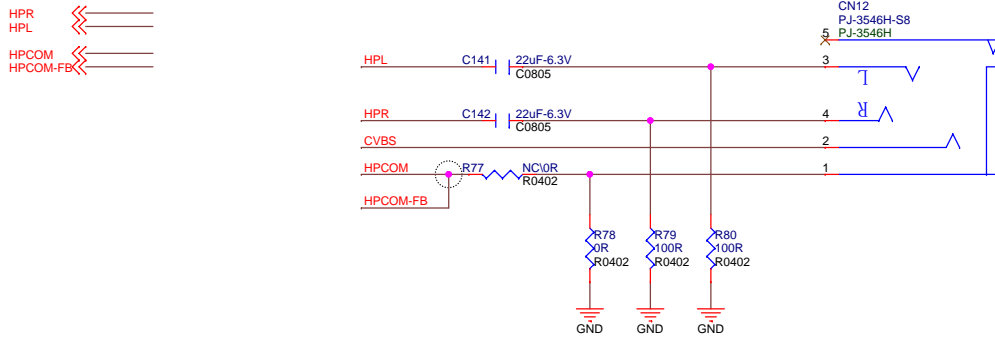


HDMI

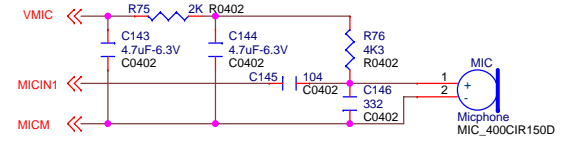


AUDIO-CVBS-LCD

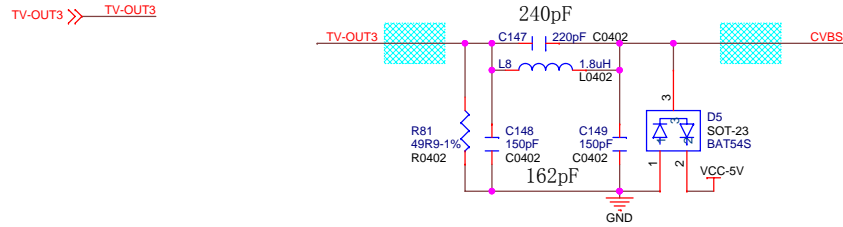
HeadSet



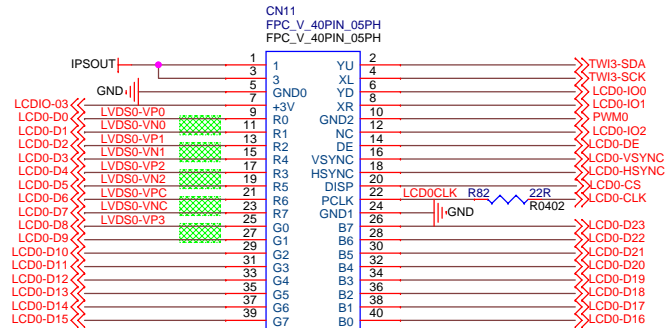
Microphone



CVBS



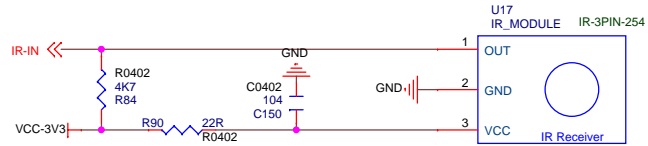
LCD



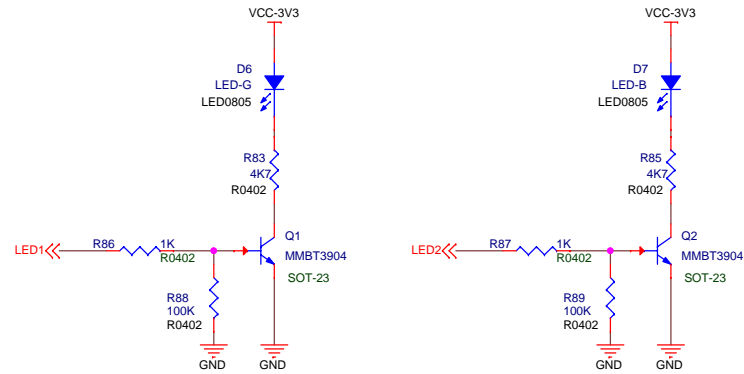
Xunlong Software		
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IR-ExtPort-LEDs

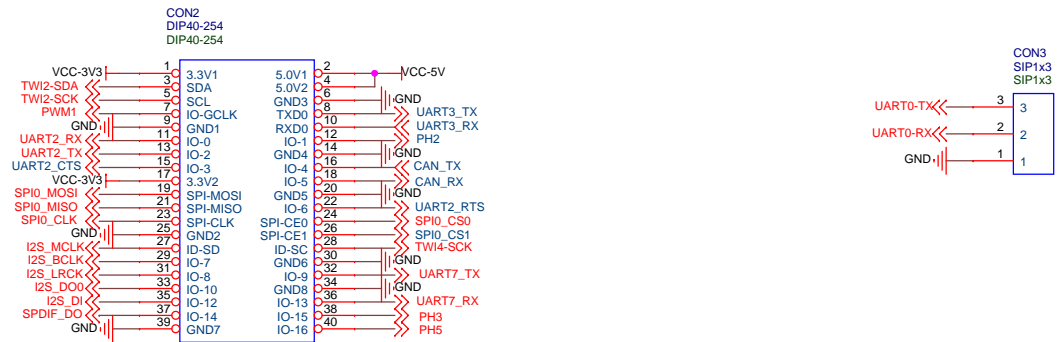
IR MODULE



LEDs



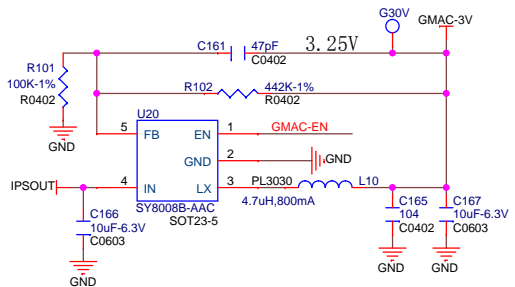
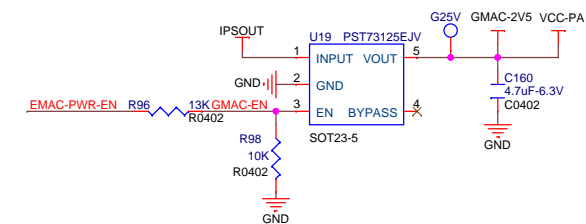
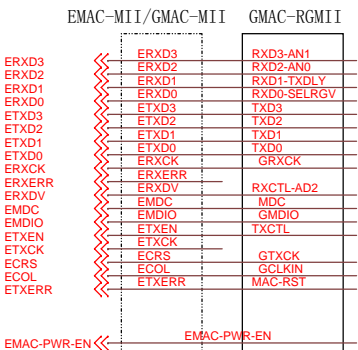
Ext Port



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GMAC

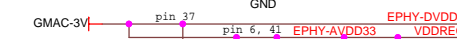
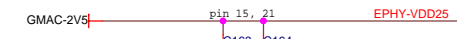
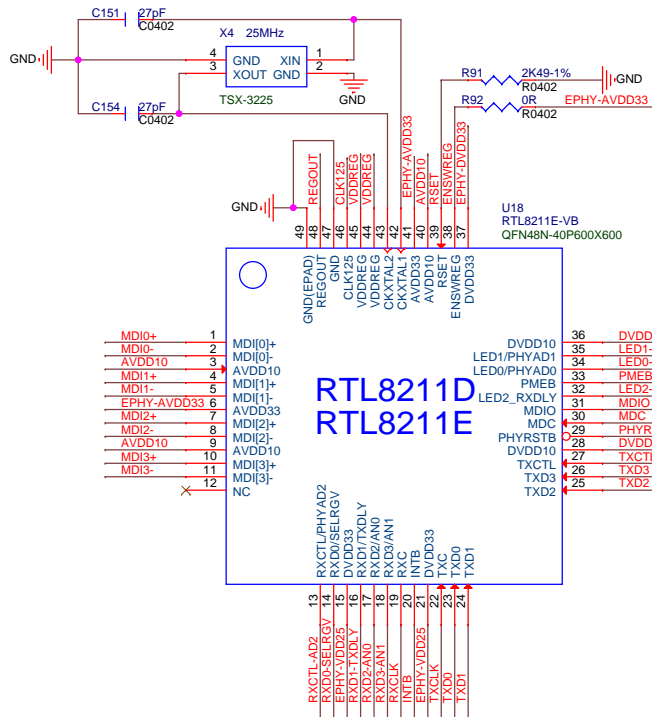
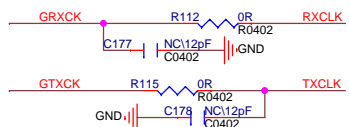
10/100/1000 RGMII Ethernet PHY



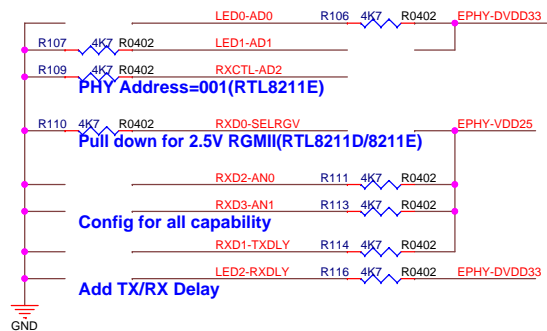
Place filter network close to CLK125.
Reserved for EMI



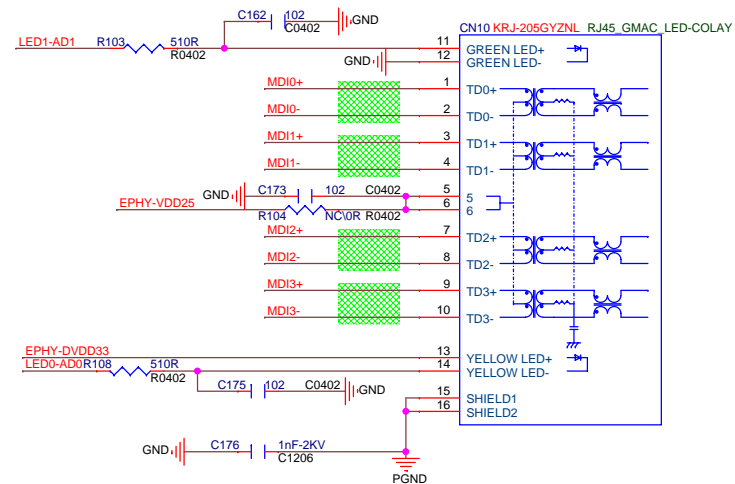
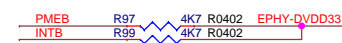
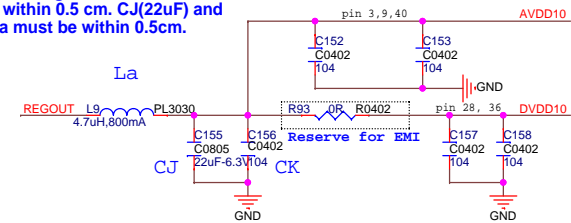
Place filter network close to RX_CLK.
Reserved for EMI



Note 2: The Trace length from CA(22uF),CB(0.1uF) to Pin 44,45(VDDREG) must be within 0.5 cm. The trace width from AVDD33 to Pin 44,45 should >40mils.



Note 1: The Trace length between La and PHY's Pin48 must be within 0.5 cm. CJ(22uF) and CK(0.1uF) to La must be within 0.5cm.



LED0: Blinking=Transmitting or Receiving.
LED1: Link Up/Down

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V1.0

一. 第一版

1. 原SD卡座改为TF卡座
2. 增加TF卡: SD3
3. 保留3个LED灯 (1个Power、2个自定义)
4. DC输入改为DC座子
5. 开关、Reset按键改为插件
6. 增加USB-HUB芯片
7. 增加USB-WiFi
8. 更改耳机座子、耳机电路增加22uF电容, CVBS改到耳机接口
9. 26PIN接口改为40PIN (27、28PIN缺少I2C接口, 用普通IO替代)
10. UART0改为3PIN接口
11. PLLVP增加3.0V LDO
12. CSI改为24PIN, 删除原40PIN的网络: FMIN、LINEIN、ADC_TP、LRADC、HP、CSI1-RESET#、CSI1-STBY-EN
13. 删除RST按键

A20	PIN	使用	不使用
HDMI	T13	VCC-3V3	VCC-3V3
SATA 2.5V	N15, N16	2.5V	DRAM-VCC(1.5V)
CSIO	F19 (VCC-PE)	VCC-2.8	VCC-2.8
CSI1	E18 (VCC-PG)	VCC-2.8	VCC-3V3
TVIN 2.5V	W17	2.5V	DRAM-VCC(1.5V)
TVIN 3.3V	W16	VCC-3V3	VCC-3V3
TVOUT 3.3V	W15	VCC-3V3	VCC-3V3
LVDS IO	W12, W13, W14	VCC-3V3	VCC-3V3
EMAC IO	J10, H10 (VCC-PA)	3.0V/2.5V	VCC-3V3
NAND IO	J19, H19 (VCC-PC)	VCC-3V3	VCC-3V3
SD IO	N19 (VCC-PF)	VCC-3V3	VCC-3V3
USB VDD	K16	INTVDD(1.2V)	VCC-3V3
USB VCC	L15, L16	VCC-3V3	VCC-3V3

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